XT-IDE Rev 4 - IDE Functionality

1 = On
0 = Off

= Jumper out
= Jumper in

J3

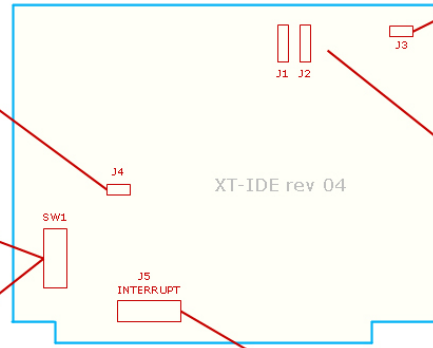
If desired, connect an external LED to J3 (observing polarity).

Just like the LED on the card, the external LED will show IDE activity.

J4

If a jumper is placed on J4, then +5 volts will be put on pin 20 of the IDE connector.

Certain (not all) DOMs and CF-to-IDE adapters can be powered that way.



SW1 - XT8

The XT8 switch is described elsewhere (because it is not IDE specific).

COMPATIBILITY MODE

Slower than hi-speed mode.

Use with 1.x.x versions of the XTIDE Universal BIOS.

If instead, version 'v2.0.0 beta 3' of the XTIDE Universal BIOS is fitted, configure its device type option to: 'XTIDE rev 1'.

HI-SPEED MODE

Also known as the 'Chuck mod'.

When configuring version 'v2.0.0 beta 3' of the XTIDE Universal BIOS, select the device type of: 'XTIDE rev 2 or modded rev 1'

1.x.x versions of the XTIDE Universal BIOS cannot be configured for hi-speed mode.

SW1 - A9 THROUGH A4
BASE I/O ADDRESS OF IDE INTERFACE

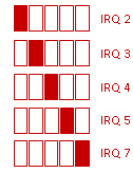
If you change your card's I/O address, then you will need to reconfigure the XTIDE Universal BIOS (so that the BIOS knows which I/O base address to use).

	200	210	220	230	240	250	260	270	280	290	2A0	2B0	2C0	2D0	2E0	2F0	300	310	320	330	340	350	360	370	380	390	3A0	3B0	3C0	3D0	3E0	3F0
A9	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
A8	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
A7	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1
A6	0	0	0	0	1	1	1	1	0	0	0	0	1	1	1	1	0	0	0	0	1	1	1	1	0	0	0	0	1	1	1	1
A5	0	0	1	1	0	0	1	1	0	0	1	1	0	0	1	1	0	0	1	1	0	0	1	1	0	0	1	1	0	0	1	1
A4	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1

DEFAULT SETTING
Address of 300 (hex)

J5

IDE interrupt



Not presently supported by the XTIDE Universal BIOS.

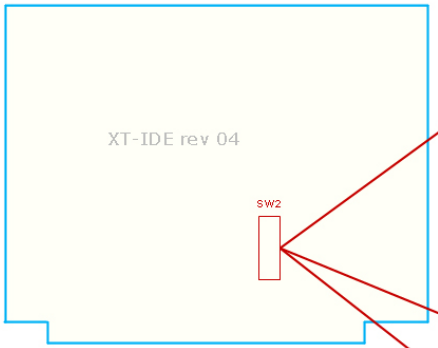
No point in jumpering.

XT-IDE Rev 4 - Optional BIOS ROM (Boot ROM)

On this card, the BIOS ROM is an EEPROM containing 'XTIDE Universal BIOS' software.

28C64 = 8 KB sized EEPROM
28C256 = 32 KB sized EEPROM

Fitment to this card is optional if the XTIDE Universal BIOS resides elsewhere in the computer.
If elsewhere, disable the BIOS ROM functionality on this card by turning off the ENA switch.



BASE ADDRESS OF BIOS ROM

	28C64 type ROM												28C256 type ROM		
	C8	CA	CC	CE	D0	D2	D4	D6	D8	DA	DC	DE	C8	D0	D8
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
A16	**0**	**0**	**0**	**0**	**1**	**1**	**1**	**1**	**1**	**1**	**1**	**1**	**0**	**1**	**1**
A15	**1**	**1**	**1**	**1**	**0**	**0**	**0**	**0**	**1**	**1**	**1**	**1**	**1**	**0**	**1**
A14	**0**	**0**	**1**	**1**	**0**	**0**	**1**	**1**	**0**	**0**	**1**	**1**	**0**	**0**	**0**
A13	**0**	**1**	**0**	**1**	**0**	**1**	**0**	**1**	**0**	**1**	**0**	**1**	**0**	**0**	**0**

DEFAULT SETTING
Address of D0000 (hex)

DEFAULT SETTING
Address of D0000 (hex)

BIOS ROM TYPE

	28	28
	8	C
	C	2
	6	5
	4	6
8K	**1**	**0**
8K	**1**	**0**

1 = ON
0 = OFF

ENA : Enable BIOS ROM (the EEPROM) [Default setting is ON, to enable]

WR : Allow writes to BIOS ROM (the EEPROM) [Default setting is ON, to allow writes]